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VP Heuring, HF Jordan, M Murodca - 1997 - cs.clemson.edu

 ... **Register transfer notation - RTN** • The main function of the CPU is the **Register Transfer ...** and register transfers ... optimizations of the assembly language ...

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 No assembly required: Compiling Standard ML to C- ► [psu.edu](#) [\[PDF\]](#)

D Tarditi, P Lee, A Acharya - ACM Letters on Programming Languages and Systems (LOPLAS), 1992 - portal.acm.org

 ... 21] is lost, despite a **compile-time analysis ...** is a lexically scoped, call-by-value language with higher ... overflow checking, floating-point operations, **register-to ...**

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 PRISM-II compiler and architecture- ► [psu.edu](#) [\[PDF\]](#)

M Wazlowski, L Agarwal, T Lee, A Smith, E Lam, P ... - IEEE Workshop on FPGAs for Custom Computing Machines, 1993 ... 1993 - ieeeexplore.ieee.org

 ... the mid-70s cen- tered on dynamically changing the instruction set at **compile time** to ... used by GCC is called RTL, a variation of a **register transfer language**. ...

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The application accelerator illustration system

MS Miller, H Cunningham, C Lee, SR Vegdahl - Proceedings of the 1986 conference on Object-oriented ..., 1986 - portal.acm.org

 ... We selected the **register transfer level** of abstraction because it ... easy to understand, and is sim- ple to **compile**. A full description of the **language** is beyond ...

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An architecture for the direct execution of the Forth programming language

JR Hayes, ME Fraeman, RL Williams, T Zaremba - portal.acm.org

 ... of an extra step: edit-cross **compile**-download-debug ... primitive word defined in assembly **language** is found ... can be summarized in a **register transfer notation** shown ...

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Integrating code generation and optimization

CW Fraser, AL Wendt - Proceedings of the 1986 SIGPLAN symposium on Compiler ..., 1986 - portal.acm.org

 ... the opcodes being hashed at **compile-compile time** because ... **register transfers to assembly language** for output ... used to translate a **register transfer** to assembly ...

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 A tree-based alternative to Java byte-codes- ► [psu.edu](#) [\[PDF\]](#)

T Kistler, M Franz - International Journal of Parallel Programming, 1999 - Springer

 ... code, accelerating the **transfer** of executable ... intermodular inlining, intermodular **register allocation**, and ... tree-structured intermediate **language** in conjunction ...

Cited by 62 - Related articles - Web Search - Library Search - BI. Direct - All 19 versions

A knowledge-based compiler enhancing DSP internal parallelism

I Kuroda, A Hirano, T Nishitani - IEEE International Symposium on Circuits and Systems, 1991., 1991 - ieeexplore.ieee.org

... role of HLL(high level language) **compil- ers** for ... distributed in the complicated **data-path** having their ... whose then part is a **register transfer** level operation ...

Cited by 3 - Related articles - Web Search

Address compression through base **register** caching

A Park, M Farrens - Proceedings of the 23rd annual workshop and symposium on ..., 1990 - portal.acm.org

... using address or data lines to **transfer** the high ... be stored in a **simple register** file organization ... High level **language** compilers presently **compile** programs for ...

Cited by 22 - Related articles - Web Search - All 2 versions

Symbolic simulation for correct machine design

WC Carter, WH Joyner Jr, D Brand - Proceedings of the 16th Conference on Design automation, 1979 - portal.acm.org

... **tion language**[1,2,3,4], **compile** this description ... of a processor of machine-**language** instructions, but ... describe, also in LSS, the **register transfer** level of the ...

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Key authors: **R Camposano** - **M Wazlowski** - **V Heuring** - **D Tarditi** - **R Kelsey**

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